

The following Listing of Claims will replace all prior versions, and listings, of claims in the application.

LISTING OF CLAIMS:

1 - 11. (Cancelled).

12. (Currently Amended) A single-ended Single-ended differential amplifier circuit comprising:

first unit having first and second active devices which have respectively having first, second, and third terminals, respectively, wherein current flowing from the second terminal to the third terminal has having its quantity and direction being varying in dependant on the voltage driven to the said first terminal of said first unit, said first and second active devices being N type MOSFET;

second unit having third and fourth devices which have respectively having first, second, and third terminals, respectively, wherein current flowing from the second terminal to the third terminal has having its quantity and direction being varying in dependant on the voltage driven to the said first terminal of said second unit, said third and fourth active devices are P type MOSFET; and

biasing means being connected to the said first terminals of the said first and third active devices and an input terminal, and being connected to the said second terminals of the said second and fourth active devices and an output terminal, for determining biasing points of the said first and second units, such that the said first and second units operates operating in a differential relationship with respect to a signal driven to said input terminal, wherein

said biasing means determines the determining said biasing points such that one of the said first and second units is substantially active.

13. (Currently Amended) The single-ended differential amplifier circuit of Claim 12, wherein said biasing means determines biasing points of the first and second units such that current flowing from the first active device to the second active devices device of the first unit is in opposite phase to current flowing from the third active device to the fourth active device of the second unit.

14. (Cancelled).

15. (New) A single-ended differential amplifier circuit comprising:
a first unit having first and second active devices respectively having first, second, and third terminals, current flowing from said second terminal to said third terminal having its quantity and direction varying dependant on the voltage driven to said first terminal of said first unit, said first and second active devices being N type MOSFET;
a second unit having third and fourth devices respectively having first, second, and third terminals, current flowing from said second terminal to said third terminal of said second unit having its quantity and direction varying dependant on the voltage driven to said first terminal of said second unit, said third and fourth active devices are P type MOSFET;
and

a biasing and matching circuit being connected to said first terminals of said first and third active devices and an input terminal, and being connected to said second terminals of said second and fourth active devices and an output terminal, to determine biasing points of

said first and second units, said first and second units operating in a differential relationship with respect to a signal driven to said input terminal, said biasing and matching circuit determining said biasing points such that one of said first and second units is substantially active.

16. (New) The single-ended differential amplifier circuit of Claim 12, wherein said biasing and matching circuit determines biasing points of said first and second units such that current flowing from said first active device to said second active device of said first unit is in opposite phase to current flowing from said third active device to said fourth active device of said second unit.